

Serial No. 09/553756 Filing Date 4/21/00 OFGS File No. IR-1888(2-233)
Title Circuitry For High Voltage Linear Current Sense IC
First Inventor Joseph Maggolino Date 11/19/01
The PTO has received: Last Due Date: _____

- ☐ Patent Application of _____
Pages (including claims & abstract)
☐ Declaration or ☐ Designation Sheet
☐ Drawings _____ Sheet(s)/Figs. _____ to _____
☐ Priority Document
☐ Small Entity Declaration
☐ Assignment & Conveyance Cover Sheet
☐ Information Disclosure Statement
☐ PTO-1449
☒ Amendment Rule 312
☐ Affidavit or Declaration
☐ _____

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- ☐ TM Application ☐ ITU Basis
☐ Statement of Use
☐ Extension of Time
☐ Notice of Appeal
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☐ Petition
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Joseph MAGGIOLINO

Date: November 19, 2001

Serial No.: 09/553,756

Group Art Unit: 2816

Filed: April 21, 2000

Examiner: A. Tra

For: CIRCUITRY FOR HIGH VOLTAGE LINEAR CURRENT SENSE IC

Commissioner for Patents

Washington, D.C. 20231

RULE 312 AMENDMENT

Pursuant to the provisions of 37 C.F.R. § 312(a), please amend the above-identified application as follows:

AMENDMENTS

1. X If checked, amendment(s) to the specification and/or claims are submitted herewith.

 If checked, an abstract is submitted as the last page of Appendix A.

2. Claims:

Please amend claim 1 pursuant to 37 C.F.R. § 1.121(c)(i) as set forth in the "clean" version attached hereto as Appendix A. Entry is respectfully requested. A version with markings to show the changes made pursuant to 37 C.F.R. § 1.121(c)(ii) is attached hereto as Appendix B.

3. Clean copy of claims:

An optional complete set of "clean" claims pursuant to 37 C.F.R. § 1.121(c)(3) is attached hereto as Appendix C.